

3-11-05

<b>FORM PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> Express Mail Number <u>EV36403344US</u> (Use several sheets if necessary)	Docket Number (Optional) 100-15120 (P04976-P01-D01)	Application No. New (Div. of 09/994,293) <u>10/81501</u>
	Applicant(s) Abdalla Aly Naem	
	Filing Date Herewith	Group Art Unit Unknown

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<u>EDJ</u>	*5 3 2 3 0 3 2	06/21/94	Sato et al.	257	198	09/08/92
	*5 3 2 0 9 7 2	06/14/94	Wyllie	437	31	01/07/93
	*4 5 6 4 9 9 7	01/21/86	Matsuo et al.	29	576W	04/16/82
	*6 3 5 2 9 0 7	03/05/02	Gris	438	438	05/26/00
	*6 1 7 1 9 3 6	01/09/01	Fitzgerald	438	503	01/04/99
	*5 8 9 7 3 5 9	04/27/99	Cho et al.	438	312	12/09/97
	*5 6 4 8 2 8 0	07/15/97	Kato	437	31	09/26/95
	*5 3 6 2 6 6 9	11/08/94	Boyd et al.	437	67	06/24/93
	*5 0 7 7 2 2 7	12/31/91	Kameyama et al.	437	31	08/08/90
	*6 4 2 6 2 6 5	07/30/02	Chu et al.	438	312	01/30/01
	*5 5 0 8 2 1 3	04/16/96	Van Der Wel et al.	437	31	10/20/94
<u>EDJ</u>	*5 1 8 7 5 5 4	02/16/93	Miwa	307	586	04/29/91

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS S	Translation Yes No

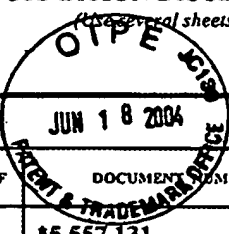
## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<u>EDJ</u>	C.A. King et al., "Very Low Cost Graded SiGe Base Bipolar Transistors for a High-Performance Modular BiCMOS Process", IEDM, 1999, pps. 565-568.
<u>EDJ</u>	Wim van der Wel et al., "Poly-Ridge Emitter Transistor (PRET): Simple Low-Power Option to a Bipolar Process", IEDM, 1993, pps. 453-456.

EXAMINER <u>EDJ</u>	DATE CONSIDERED <u>6-10-05</u>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

100-15120 (P04976-P01-D01)

Application Number

10/821,501

Applicant(s)

Abdalla Aly Naem

Filing Date

April 9, 2004

Group Art Unit

Unknown

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
ED		*5,557,131	09/17/96	Lee	257	370	07/14/94
		*6,468,871	10/22/02	Naem	438	378	03/23/01
SM		*6,235,601 B1	05/22/01	Kim	438	348	09/19/97

## U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

ED		Asanga H. Perera et al., "Influence of Back-End Thermal Processing on Polysilicon-Monosilicon Contact Resistance Due to Dopant Deactivation", Proc. of the Bipolar/BiCMOS Circuits and Technology Meeting", 1994, pps. 242-245.						
ED		M. Racanelli et al., "Contact Technology for High Performance Scalable BiCMOS on TFSOI, IEEE Electron Device Letters, Vol. 16, No. 10, October 1995, pps. 424-426.						

EXAMINER

ED

DATE CONSIDERED

6-10-05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

